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U.S. UTILITY Patent Application

APPL NUM 10053545	FILING DATE 01/24/2002	CLASS 707 71	SUBCLASS 104	GAU 2171	EXAMINER M. P. N. N. N.
<b>**APPLICANTS:</b> Kumaki Satoshi; Matsumura Tetsuya; Segawa Hiroshi; Hanami Atsuo; Mosneaga Vasile;					
<b>**CONTINUING DATA VERIFIED:</b>					
<b>** FOREIGN APPLICATIONS VERIFIED:</b> JAPAN 2001-172339(P) 06/07/2001					
PG-PL3		DO NOT PUBLISH <input type="checkbox"/>		RESCIND <input type="checkbox"/>	
Foreign priority claimed		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no		ATTORNEY DOCKET NO	
35 USC 119 conditions met		<input checked="" type="checkbox"/> yes <input type="checkbox"/> no		57454-334	
Verified and Acknowledged Examiners's initials					
<b>TITLE :</b> Data processor and data processing method reduced in power consumption during memory access					

U.S. DEPT. OF COMM. / PAT. & TM. PTO-436L (Rev. 12-94)

<b>NOTICE OF ALLOWANCE MAILED</b>		Assistant Examiner	<b>CLAIMS ALLOWED</b>	
			Total Claims	Print Claim for O.G.
<b>ISSUE FEE</b>		Primary Examiner	<b>DRAWING</b>	
Amount Due	Date Paid		Sheets Drwg.	Figs. Drwg.
<input type="checkbox"/> <b>TERMINAL DISCLAIMER</b>		<b>PREPARED FOR ISSUE</b>	Application Examiner	
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